

Rf Microelectronics 2nd Edition

This volume has its beginnings in a laboratory project, development of a radiolocator for the Wi-Fi network that was growing by leaps and bounds on the campus of Indiana University at that time. What started as a very focused and practical attempt to improve network management, touched in its lifetime upon broader issues of the use of radio spectrum, design of system architectures for the wireless medium, and image formation outside the limits of geometrical optics.

I have intended this book mostly for the audience of engineers and system designers, in the growing field of radio communication among small, portable, ubiquitous devices that have become hybrid platforms for personal communication and personal computing. It is also a book addressed to network professionals, people to whom radio is largely a black box, a medium that they usually rely upon, but seldom fully understand. In fact, in the course of my work in the field, I have witnessed, to my dismay, a wide disconnect between the networking world and the radio technology that networking has come to depend upon so heavily. Perhaps, because digital wireless communication is seen as digital first and wireless second, there is often a misplaced emphasis on its information-processing side, with the methodology centered around the discrete symbol, and with little intuition of the underlying physics. I had it once suggested to me, in apparent seriousness, to use radio cards for intra-system communication within a radiolocator! Wireless communication is radio, plain and simple.

The book is a collection of high-quality peer-reviewed research papers presented in the first International Conference on International Conference on Artificial Intelligence and Evolutionary Computations in Engineering Systems (ICAIECES -2015) held at Velammal Engineering College (VEC), Chennai, India during 22 – 23 April 2015. The book discusses wide variety of industrial, engineering and scientific applications of the emerging techniques. Researchers from academic and industry present their original work and exchange ideas, information, techniques and applications in the field of Communication, Computing and Power Technologies.

Today's integrated silicon circuits and systems for wireless communications are of a huge complexity. This unique compendium covers all the steps (from the system-level to the transistor-level) necessary to design, model, verify, implement, and test a silicon system. It bridges the gap between the system-world and the transistor-world (between communication, system, circuit, device, and test engineers). It is extremely important nowadays (and will be more important in the future) for communication, system, and circuit engineers to understand the physical implications of system and circuit solutions based on hardware/software co-design as well as for device and test engineers to cope with the system and circuit requirements in terms of power, speed, and data throughput. [Related Link\(s\)](#)

This comprehensive resource explains the theory of RF circuits and systems and the practice of designing them. The

fundamentals for linear and low noise amplifier designs, including the S and noise parameters and their applications in amplifier designs and matching network designs using the Smith chart are covered. Theories of RF power amplifiers and high efficiency power amplifiers are also explained. The underpinnings of wireless communications systems as well as passive components commonly used in RF circuits and measurements are discussed. RF measurement techniques and RF switches are also presented. The book explores stability criteria and the invariant property of lossless networks and includes detailed theoretical treatments. The basic concepts and techniques covered in this book are routinely used in today's engineering practice, especially from the perspective of printed circuit board (PCB) based RF circuit design and system integration. Intended for practicing engineers and circuit designers, this book focuses on practical topics in circuit design and measurement techniques. It bridges the gap between academic materials and real circuit designs using real circuit examples and practical tips. Readers develop a numerical feel for RF problems as well as awareness of the concepts of design for cost and design for manufacturing, which is a critical skill set for today's engineers working in an environment of commercial product development.

Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The book's unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

This comprehensive new resource guides professionals in the latest methods used when designing active integrated antennas (AIA) for wireless communication devices for various standards. This book provides complete design procedures for the various elements of such active integrated antennas such as the matching network, the amplifier/active element as well as the antenna. This book offers insight into how active integration and co-design between the active components (amplifier, oscillator, mixer, diodes) and the antenna can provide better power transfer, higher gains, increased efficiencies, switched beam patterns and smaller design footprints. It introduces the co-design approach of active integrated antennas and its superior performance over conventional methods. Complete design examples are given of active integrated antenna systems for narrow and wideband applications as well as for multiple-input-multiple-output (MIMO) systems. Readers find the latest design methods for narrow and broadband RF matching networks. This book provides a complete listing of performance metrics for active integrated antennas. The book serves as a complete reference and design guide in the area of AIA.

Modern transceiver systems require diversified design aspects as various radio and sensor applications have emerged.

Choosing the right architecture and understanding interference and linearity issues are important for multi-standard cellular transceivers and software-defined radios. A millimeter-wave complementary metal–oxide–semiconductor (CMOS) transceiver design for multi-Gb/s data transmission is another challenging area. Energy-efficient short-range radios for body area networks and sensor networks have recently received great attention. To meet different design requirements, gaining good system perspectives is important. *Wireless Transceiver Circuits: System Perspectives and Design Aspects* offers an in-depth look at integrated circuit (IC) design for modern transceiver circuits and wireless systems. Ranging in scope from system perspectives to practical circuit design for emerging wireless applications, this cutting-edge book: Provides system design considerations in modern transceiver design Covers both systems and circuits for the millimeter-wave transceiver design Introduces four energy-efficient short-range radios for biomedical and wireless connectivity applications Emphasizes key building blocks in modern transceivers and transmitters, including frequency synthesizers and digital-intensive phase modulators Featuring contributions from renowned international experts in industry and academia, *Wireless Transceiver Circuits: System Perspectives and Design Aspects* makes an ideal reference for engineers and researchers in the area of wireless systems and circuits.

This book is based on the 18 tutorials presented during the 23rd workshop on Advances in Analog Circuit Design. Expert designers present readers with information about a variety of topics at the frontier of analog circuit design, serving as a valuable reference to the state-of-the-art, for anyone involved in analog circuit research and development.

Nowadays, the implementation of novel technological platforms in biosensor-based developments is primarily directed to the miniaturization of analytical systems and lowering the limits of detection. Rapid scientific and technological progress enables the application of biosensors for the online detection of minute concentrations of different chemical compounds in a wide selection of matrixes and monitoring extremely low levels of biomarkers even in living organisms and individual cells. This book, including 16 chapters, characterizes the present state of the art and prospective options for micro and nanoscale activities in biosensors construction and applications.

As a result of higher frequencies and increased user mobility, researchers and systems designers are shifting their focus from time-invariant models to channels that vary within a block. *Wireless Communications Over Rapidly Time-Varying Channels* explains the latest theoretical advances and practical methods to give an understanding of rapidly time varying channels, together with performance trade-offs and potential performance gains, providing the expertise to develop future wireless systems technology. As well as an overview of the issues of developing wireless systems using time-varying channels, the book gives extensive coverage to methods for estimating and equalizing rapidly time-varying channels, including a discussion of training data optimization, as well as providing models and transceiver methods for time-varying ultra-wideband channels. An introduction to time-varying channel models gives in a nutshell the important issues of developing wireless systems technology using time-varying channels Extensive coverage of methods for estimating and equalizing rapidly time-varying channels, including a discussion of training data optimization, enables development of high performance wireless systems Chapters on transceiver design for OFDM and receiver algorithms for MIMO communication channels over time-varying channels, with an emphasis on modern

iterative turbo-style architectures, demonstrates how these important technologies can optimize future wireless systems

Antennas are essential part of every wireless communication system. The increasing trend of applications in the radio frequency (RF) and millimeter wave frequency spectrum has reduced the antenna sizes to only a few millimeters, which makes it practical for on-chip implementations. Integrated Circuit (IC) designers who have traditionally remained isolated from antenna design now need to understand its design process and trade-offs. This comprehensive resource addresses the challenges, benefits and trade-offs of on-chip antenna implementation. It presents practical design and integration considerations of the IC and antenna combination and how both ends of the system can be utilized in a complimentary way. The book includes on-chip antenna layout considerations, layout for testability and various methods of their characterization. A look at the future trends and utilization of on-chip antennas for different applications concludes the book.

The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest Architectures, Circuits, and Devices Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In RF Microelectronics, Second Edition, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now Offers a stronger tutorial focus along with hundreds of examples and problems Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noise-power-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low ...

This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double-Pole Four-Throw (DP4T) RF switches for next generation communication systems. The authors discuss the design of the (DP4T) RF switch by using the Double-Gate (DG) MOSFET, as well as the Cylindrical Surrounding double-gate (CSDG) MOSFET. The effect of HFO₂ (high dielectric material) in the design of DG MOSFET and CSDG MOSFET is also explored. Coverage includes comparison of Single-gate MOSFET and Double-gate MOSFET switching parameters, as well as testing of MOSFETs parameters using image acquisition.

This thoroughly revised textbook provides the fundamentals of spread-spectrum systems with a continued emphasis on theoretical principles. The revision includes new sections and appendices on characteristic functions and Laplace transforms, orthonormal expansions of functions,

the SNR wall in detection, multiple-input multiple-output systems, multicode and multirate systems, interference cancelers, complementary codes, chaos and ultrawideband systems, and the normalized LMS algorithm. As with previous editions, the author presents topics in a practical way that is of interest to both researchers and system designers. He includes updated problems at the end of each chapter, which are intended to assist readers in consolidating their knowledge and to provide practice in analytical techniques. In addition to the new and revised material, the author adds 50 new pages to make the book more accessible to graduate students in electrical engineering.

This book provides in-depth coverage of transformer-based design techniques that enable CMOS oscillators and frequency dividers to achieve state-of-the-art performance. Design, optimization, and measured performance of oscillators and frequency dividers for different applications are discussed in detail, focusing on not only ultra-low supply voltage but also ultra-wide frequency tuning range and locking range. This book will be an invaluable reference for anyone working or interested in CMOS radio-frequency or mm-Wave integrated circuits and systems.

Advances in Analog and RF IC Design for Wireless Communication Systems gives technical introductions to the latest and most significant topics in the area of circuit design of analog/RF ICs for wireless communication systems, emphasizing wireless infrastructure rather than handsets. The book ranges from very high performance circuits for complex wireless infrastructure systems to selected highly integrated systems for handsets and mobile devices. Coverage includes power amplifiers, low-noise amplifiers, modulators, analog-to-digital converters (ADCs) and digital-to-analog converters (DACs), and even single-chip radios. This book offers a quick grasp of emerging research topics in RF integrated circuit design and their potential applications, with brief introductions to key topics followed by references to specialist papers for further reading. All of the chapters, compiled by editors well known in their field, have been authored by renowned experts in the subject. Each includes a complete introduction, followed by the relevant most significant and recent results on the topic at hand. This book gives researchers in industry and universities a quick grasp of the most important developments in analog and RF integrated circuit design.

Emerging research topics in RF IC design and its potential application Case studies and practical implementation examples Covers fundamental building blocks of a cellular base station system and satellite infrastructure Insights from the experts on the design and the technology trade-offs, the challenges and open questions they often face References to specialist papers for further reading

This second edition of the highly acclaimed RF Power Amplifiers has been thoroughly revised and expanded to reflect the latest challenges associated with power transmitters used in communications systems. With more rigorous treatment of many concepts, the new edition includes a unique combination of class-tested analysis and industry-proven design techniques. Radio frequency (RF) power amplifiers are the fundamental building blocks used in a vast variety of wireless communication circuits, radio and TV broadcasting transmitters, radars, wireless energy transfer, and industrial processes. Through a combination of theory and practice, RF Power Amplifiers, Second Edition provides a solid understanding of the key concepts, the principle of operation, synthesis, analysis, and design of RF power amplifiers. This extensive update boasts: up to date end of chapter summaries; review questions and problems; an expansion on key concepts; new examples related to real-world applications illustrating key concepts and brand new chapters covering 'hot topics' such as RF LC oscillators and dynamic power supplies. Carefully edited for superior readability, this work remains an essential reference for research & development staff and design engineers. Senior level undergraduate and graduate electrical engineering students will also find it an invaluable resource with its practical examples & summaries, review questions and end of chapter problems. Key features: • A fully revised solutions manual is now hosted on a companion website alongside new simulations. • Extended treatment of a broad range of topologies of RF power amplifiers.

- In-depth treatment of state-of-the art of modern transmitters and a new chapter on oscillators.
- Includes problem-solving methodology, step-by-step derivations and closed-form design equations with illustrations.

Examining the most important developments in highly integrated wireless RF front ends, this book describes and evaluates both active and passive solutions for on-chip high-Q filtering, and explores M-phase filters in depth. An accessible step-by-step approach is used to introduce everything an RF designer needs to know about these filters, including their various forms, principles of operation, and their performance against implementation-related imperfections. Real-world examples are described in depth, and detailed mathematical analyses demonstrate the practical quantification of pertinent circuit parameters.

This book presents a novel approach to the analysis and design of all-digital phase-locked loops (ADPLLs), technology widely used in wireless communication devices. The authors provide an overview of ADPLL architectures, time-to-digital converters (TDCs) and noise shaping. Realistic examples illustrate how to analyze and simulate phase noise in the presence of sigma-delta modulation and time-to-digital conversion. Readers will gain a deep understanding of ADPLLs and the central role played by noise-shaping. A range of ADPLL and TDC architectures are presented in unified manner. Analytical and simulation tools are discussed in detail. Matlab code is included that can be reused to design, simulate and analyze the ADPLL architectures that are presented in the book.

The book presents the analysis and design of integrated automotive radar receivers in Silicon-Germanium technology, for use in complex multi-channel radar transceiver front-ends in the 77GHz frequency band. The main emphasis of the work is the realization of high-linearity and low-power modular receiver channels as well as the investigation of millimeter-wave integrated test concepts for the receiver front-end. A guide to the theory and recent development in the medical use of antenna technology *Antenna and Sensor Technologies in Modern Medical Applications* offers a comprehensive review of the theoretical background, design, and the latest developments in the application of antenna technology. Written by two experts in the field, the book presents the most recent research in the burgeoning field of wireless medical telemetry and sensing that covers both wearable and implantable antenna and sensor technologies. The authors review the integrated devices that include various types of sensors wired within a wearable garment that can be paired with external devices. The text covers important developments in sensor-integrated clothing that are synonymous with athletic apparel with built-in electronics. Information on implantable devices is also covered. The book explores technologies that utilize both inductive coupling and far field propagation. These include minimally invasive microwave ablation antennas, wireless targeted drug delivery, and much more. This important book: Covers recent developments in wireless medical telemetry Reviews the theory and design of in vitro/in vivo testing Explores emerging technologies in 2D and 3D printing of antenna/sensor fabrication Includes a chapter with an annotated list of the most comprehensive and important references in the field Written for students of engineering and antenna and sensor engineers, *Antenna and Sensor Technologies in Modern Medical Applications* is an essential guide to understanding human body interaction with antennas and sensors.

This book presents state-of-the-art techniques for radiation hardened high-resolution Time-to-Digital converters and low noise frequency synthesizers. Throughout the book, advanced degradation mechanisms and error sources are discussed and several ways to prevent such errors are presented. An overview of the prerequisite physics of nuclear interactions is given that has been compiled in an easy to understand chapter. The book is structured in a way that different hardening techniques and solutions are supported by theory and experimental data with their various tradeoffs. Based on leading-edge research, conducted in collaboration between KU Leuven and CERN, the European Center for Nuclear Research Describes in detail advanced techniques to harden circuits against ionizing radiation Provides a practical way to

learn and understand radiation effects in time-based circuits Includes an introduction to the underlying physics, circuit design, and advanced techniques accompanied with experimental data

Using a modern, pedagogical approach, this textbook gives students and engineers a comprehensive and rigorous knowledge of CMOS phase-locked loop (PLL) design for a wide range of applications. It features intuitive presentation of theoretical concepts, built up gradually from their simplest form to more practical systems; broad coverage of key topics, including oscillators, phase noise, analog PLLs, digital PLLs, RF synthesizers, delay-locked loops, clock and data recovery circuits, and frequency dividers; tutorial chapters on high-performance oscillator design, covering fundamentals to advanced topologies; and extensive use of circuit simulations to teach design mentality, highlight design flaws, and connect theory with practice. Including over 200 thought-provoking examples highlighting best practices and common pitfalls, 250 end-of-chapter homework problems to test and enhance the readers' understanding, and solutions and lecture slides for instructors, this is the perfect text for senior undergraduate and graduate-level students and professional engineers who want an in-depth understanding of PLL design.

This book presents the challenges and solutions of designing power amplifiers at RF and mm-Wave frequencies in a silicon-based process technology. It covers practical power amplifier design methodologies, energy- and spectrum-efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications, and power amplifier and power generation designs for enabling new communication and sensing applications in the mm-Wave and THz frequencies. With this book you will learn: Power amplifier design fundamentals and methodologies Latest advances in silicon-based RF power amplifier architectures and designs and their integration in wireless communication systems State-of-the-art mm-Wave/THz power amplifier and power generation circuits and systems in silicon Extensive coverage from fundamentals to advanced design topics, focusing on various layers of abstraction: from device modeling and circuit design strategy to advanced digital and mixed-signal architectures for highly efficient and linear power amplifiers New architectures for power amplifiers in the cellular and wireless connectivity covering detailed design methodologies and state-of-the-art performances Detailed design techniques, trade-off analysis and design examples for efficiency enhancement at power back-off and linear amplification for spectrally-efficient non-constant envelope modulations Extensive coverage of mm-Wave power-generation techniques from the early days of the 60 GHz research to current state-of-the-art reconfigurable, digital mm-Wave PA architectures Detailed analysis of power generation challenges in the higher mm-Wave and THz frequencies and novel technical solutions for a wide range for potential applications, including ultrafast wireless communication to sensing, imaging and spectroscopy Contributions from the world-class experts from both academia and industry This newly revised and expanded edition of the 2003 Artech House classic, Radio Frequency Integrated Circuit Design, serves as an up-to-date, practical reference for complete RFIC know-how. The second edition includes numerous updates, including greater coverage of CMOS PA design, RFIC design with on-chip components, and more worked examples with simulation results. By emphasizing working designs, this book practically transports you into the authors' own RFIC lab so you can fully understand the function of each design detailed in this book. Among the RFIC designs examined are RF integrated LC-based filters, VCO automatic amplitude control loops, and fully integrated transformer-based circuits, as well as image reject mixers and power amplifiers. If you are new to RFIC design, you can

benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device. A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device. This leading-edge resource is packed with over 1,000 equations and more than 435 illustrations that support key topics."

This updated and greatly expanded second edition of the popular text *RF Circuit Design: Theory and Applications* provides a comprehensive coverage of the fundamental concepts of high-frequency circuit analysis and design. Each of the 10 chapters includes a *Practically Speaking* section in which the authors present realistic circuit examples. These carefully worked out circuits enable the reader to directly apply the theoretical aspects developed in this text. The text is self-contained and requires only a minimum amount of analog circuit design and electromagnetics; it is well-suited for junior and senior-level undergraduates as well as practicing engineers.

Over the past decade, tremendous development of wireless communications has changed human life and engineering. Considerable advancement has been made in design and architecture of related RF and microwave circuits. *Introduction to Wireless Communication Circuits* focuses on special circuits dedicated to the RF level of wireless communications. From oscillators to modulation and demodulation, and from mixers to RF and power amplifier circuits, all are presented in a sequential manner. A wealth of analytical relations is provided in the text alongside various worked out examples. Related problem sets are given at the end of each chapter. Basic concepts of RF Analog Circuit Design are developed in the book. Technical topics discussed include: - Wireless Communication System - RF Oscillators and Phase Locked Loops - Modulator and Demodulator Circuits - RF Mixers - Automatic Gain Control and Limiters - Microwave Circuits, Transmission Lines and S-Parameters - Matching Networks - Linear Amplifier Design and Power Amplifiers - Linearization Techniques This textbook is intended for advanced undergraduate and graduate students, as well as RF Engineers and professionals.

The book offers unique insight into the modern world of wireless communication that included 5G generation, implementation in Internet of Things (IoT), and emerging biomedical applications. To meet different design requirements, gaining perspective on systems is important. Written by international experts in industry and academia, the intended audience is practicing engineers with some electronics background. It presents the latest research and practices in wireless communication, as industry prepares for the next evolution towards a trillion interconnected devices. The text further explains how modern RF wireless systems may handle such a large number of wireless devices. Covers modern wireless technologies (5G, IoT), and emerging biomedical applications Discusses novel RF systems, CMOS low power circuit implementation, antennae arrays, circuits for medical imaging, and many other emerging technologies in wireless

co-space. Written by a mixture of top industrial experts and key academic professors.

Communication devices such as smart phones, GPS systems, and Bluetooth, are now part of our daily lives more than ever before. As our communication equipment becomes more sophisticated, so do the radios and other hardware required to enable that technology. Common radio architectures are required to make this technology work seamlessly. This resource describes practical aspects of radio frequency communications systems design, bridging the gap between system-level design considerations and circuit-level design specifications. Industry experts not only provide detailed calculations and theory to determine block level specifications, but also discuss basic theory and operational concepts. This resource also includes extensive, up-to-date application examples.

A self-contained guide to microwave electronics, covering passive and active components, linear, low-noise and power amplifiers, microwave measurements, and CAD techniques. It is the ideal text for graduate and senior undergraduate students taking courses in microwave and radio-frequency electronics, as well as professional microwave engineers. Frequency Modulated Continuous Wave (FMCW) radars are a fast expanding area in radar technology due to their stealth features, extremely high resolutions, and relatively clutter free displays. This groundbreaking resource offers engineers expert guidance in designing narrowband FMCW radars for surveillance, navigation, and missile seeking. It also provides professionals with a thorough understanding of underpinnings of this burgeoning technology. Moreover, readers find detailed coverage of the RF components that form the basis of radar construction. Featuring clear examples, the book presents critical discussions on key applications. Practitioners learn how to use time-saving MATLAB® and SystemVue design software to help them with their challenging projects in the field. Additionally, this authoritative reference shows engineers how to analyze FMCW radars of various types, including missile seekers and missile altimeters. Packed with over 600 equations, the book presents discussions on key radar algorithms and their implementation, as well as designing modern radar to meet given operational requirements.

The 1st volume of 'Advances in Microelectronics: Reviews' Book Series contains 19 chapters written by 72 authors from academia and industry from 16 countries. With unique combination of information in each volume, the 'Advances in Microelectronics: Reviews' Book Series will be of value for scientists and engineers in industry and at universities. In order to offer a fast and easy reading of the state of the art of each topic, every chapter in this book is independent and self-contained. All chapters have the same structure: first an introduction to specific topic under study; second particular field description including sensing applications. Each of chapter is ending by well selected list of references with books, journals, conference proceedings and web sites. This book ensures that readers will stay at the cutting edge of the field and get the right and effective start point and road map for the further researches and developments.

Expert contributors drawn from the ranks of academia and industry have authored chapters in such areas as third-generation wireless, wireless sensor networks, RF power amplifiers, spread spectrum modulation, signal propagation, antennas, and other key subjects that engineers working in RF and wireless need to be familiar with. This is far more than just a tutorial or reference guide—it is a "guided tour" through the world of cutting-edge RF and wireless design, combining theory, applications, and philosophies behind the RF/wireless design process. The multiple and sometimes overlapping chapters reiterate and emphasize the fundamentals in the context of different types of wireless applications. Here are just a few benefits that readers will gain from reading this book: *A refresher and update of wireless principles and techniques. *Information about the latest (and forthcoming) RF and wireless circuits, products and systems. *Guidelines, approaches, and techniques to RF/wireless design. *Examples of typical applications with an emphasis on real-world situations including existing and forthcoming new components and integrated circuits. *Coverage of new and emerging wireless topics heretofore not widely covered in print (e.g. UWB, RFID, IR, etc.) * A comprehensive survey of current RF and wireless engineering practice * Heavy emphasis on practical applications and design guidelines * Multiple contributors assure a wide range of perspectives and avoids individual bias

This book addresses in-depth technical issues, limitations, considerations and challenges facing millimeter-wave (MMW) integrated circuit and system designers in designing MMW wireless communication systems from the complementary metal-oxide semiconductor (CMOS) perspective. It offers both a comprehensive explanation of fundamental theories and a broad coverage of MMW integrated circuits and systems. CMOS Millimeter-Wave Integrated Circuits for Next Generation Wireless Communication Systems is an excellent reference for faculty, researchers and students working in electrical and electronic engineering, wireless communication, integrated circuit design and circuits and systems. While primarily written for upper-level undergraduate courses, it is also an excellent introduction to the subject for instructors, graduate students, researchers, integrated circuit designers and practicing engineers. Advanced readers could also benefit from this book as it includes many recent state-of-the-art MMW circuits. The increasing demand for extremely high-data-rate communications has urged researchers to develop new communication systems. Currently, wireless transmission with more than one Giga-bits-per-second (Gbps) data rates is becoming essential due to increased connectivity between different portable and smart devices. To realize Gbps data rates, millimeter-wave (MMW) bands around 60 GHz is attractive due to the availability of large bandwidth of 9 GHz. Recent research work in the Gbps data rates around 60 GHz band has focused on short-range indoor applications, such as uncompressed video transfer, high-speed file transfer between electronic devices, and communication to and from kiosk. Many of these applications are limited to 10 m or less, because of the huge free space path loss and oxygen absorption for 60 GHz band MMW signal. This book introduces new knowledge and novel circuit techniques to design low-power MMW circuits and systems. It also focuses on unlocking the potential applications of the 60 GHz band for high-speed outdoor applications. The innovative design application significantly improves and enables high-data-rate low-cost communication links between two access points seamlessly. The 60 GHz transceiver system-on-chip provides an alternative solution to upgrade existing networks without introducing any building renovation or external network

laying works.

The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest Architectures, Circuits, and Devices
Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In RF Microelectronics, Second Edition, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now Offers a stronger tutorial focus along with hundreds of examples and problems Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noise-power-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels Two chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing

This book shows readers how to design semiconductor devices using the most common and lowest cost logic CMOS processes. Readers will benefit from the author's extensive, industrial experience and the practical approach he describes for designing efficiently semiconductor devices that typically have to be implemented using specialized processes that are expensive, time-consuming, and low-yield. The author presents an integrated picture of semiconductor device physics and manufacturing techniques, as well as numerous practical examples of device designs that are tried and true.

This highly-anticipated second edition of an Artech House classic covers several key radar analysis areas: the radar range equation, detection theory, ambiguity functions, waveforms, antennas, active arrays, receivers and signal processors, CFAR and chaff analysis. Readers will be able to predict the detection performance of a radar system using the radar range equation, its

various parameters, matched filter theory, and Swerling target models. The performance of various signal processors, single pulse, pulsed Doppler, LFM, NLFM, and BPSK, are discussed, taking into account factors including MTI processing, integration gain, weighting loss and straddling loss. The details of radar analysis are covered from a mathematical perspective, with in-depth breakdowns of radar performance in the presence of clutter. Readers will be able to determine the noise temperature of a multi-channel receiver as it is used in active arrays. With the addition of three new chapters on moving target detectors, inverse synthetic aperture radar (ISAR) and constant false alarm rate (CFAR) and new MATLAB codes, this expanded second edition will appeal to the novice as well as the experienced practitioner.

Applicable for bookstore catalogue

This proceeding features papers discussing big data innovation for sustainable cognitive computing. The papers feature details on cognitive computing and its self-learning systems that use data mining, pattern recognition and natural language processing (NLP) to mirror the way the human brain works. This international conference focuses on cognitive computing technologies, from knowledge representation techniques and natural language processing algorithms to dynamic learning approaches. Topics covered include Data Science for Cognitive Analysis, Real-Time Ubiquitous Data Science, Platform for Privacy Preserving Data Science, and Internet-Based Cognitive Platform. The 2nd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing (BDCC 2019) took place in Coimbatore, India on December 12-13, 2019. Contains proceedings from 2nd EAI International Conference on Big Data Innovation for Sustainable Cognitive Computing (BDCC 2019), Coimbatore, India, December 12-13, 2019; Features topics ranging from Data Science for Cognitive Analysis to Internet-Based Cognitive Platforms; Includes contributions from researchers, academics, and professionals from around the world.

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